Original document

IC TAG STRUCTURE

Patent number:

JP2001014442

Publication date:

2001-01-19

Inventor:

SHIMIZU YASUO; YAMAMOTO BAN

Applicant:

MIYOTA KK;; TAMURA ELECTRIC WORKS LTD

Classification:

- international:

G06K19/077; G06K19/07; H01L23/29; H01L23/31; H01L23/12

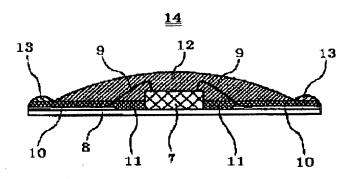
- european:

Application number: JP19990183124 19990629 Priority number(s): JP19990183124 19990629

View INPADOC patent family

Abstract of JP2001014442

PROBLEM TO BE SOLVED: To make an IC tag into structure, which is easily destroyed in the case of releasing the IC tag so as to completely disable recycling. SOLUTION: In the IC tag structure, an IC module part 14, where an IC chip 7 is mounted on a substrate 8 having a pattern part 10, the IC chip 7 and the pattern part on the substrate are connected by wire bonding and the IC chip and wire part are sealed by sealing resin, and a coil-shaped copper wire to be an antenna are provided and the IC module part and the coil-shaped copper wire are sandwiched between film members having adhesive agent, in such a case, a release layer 11 is provided between the sealing resin for sealing the IC chip 7 and the substrate 8.



Data supplied from the esp@cenet database - Worldwide

BEST AVAILABLE COPY